

Benefits of Cache Assignment on Degraded Broadcast Channels

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Abstract—The degraded K -receiver broadcast channel (BC) is studied when receivers are aided with cache memories. Lower and upper bounds are derived on the *capacity-memory tradeoff*, i.e., on the largest rate that can be achieved as a function of the receivers' cache sizes. The lower bounds are achieved by two new coding schemes that benefit from non-uniform cache assignment. The paper also provides lower and upper bounds on the *global capacity-memory tradeoff* of degraded BCs, i.e., on the largest capacity-memory tradeoff that can be attained by optimizing the receivers cache-assignment subject to a *total cache memory budget*. The bounds coincide when the total cache memory budget is sufficiently small or sufficiently large, with the thresholds depending on the BC statistics. For a small total cache budget M , it is optimal to assign all the cache memory to the weakest receiver. In this regime, the global capacity-memory tradeoff grows as $\frac{M}{D}$, where D denotes the total number of files in the system. For a large total cache budget, it is optimal to assign a positive cache memory to every receiver, where weaker receivers are assigned larger cache memories than stronger receivers. When the total cache budget M exceeds a threshold, then the global capacity-memory tradeoff grows as $\frac{1}{K} \cdot \frac{M}{D}$. A uniform cache-assignment policy is suboptimal.

I. INTRODUCTION

This paper studies the degraded broadcast channel (BC) when the receivers are equipped with cache memories and can store (i.e., cache) contents before the actual communication phase (the so called delivery phase). Our previous work in [2]–[4] on asymmetric erasure BCs with caching receivers has shown that assigning larger cache memories to the weaker receivers can improve the performance compared to the traditional uniform cache assignment that is studied in [1], [6]–[13]. In addition to mitigating the rate-bottleneck of the network, a non-uniform cache assignment creates new coding opportunities by *joint cache-channel coding* [2]–[5]. We continue the spirit embodied in the previous work, i.e., the benefit brought upon by non-uniform cache sizes, and generalize it in the channel model, coding techniques, and bounds.

The quantity of interest in this paper is the *capacity-memory tradeoff*, i.e., the largest rate R permitting reliable communication as a function of the cache sizes. We provide upper and lower bounds on the capacity-memory tradeoff for degraded BCs. The lower bounds are obtained by two new coding schemes that exploit unequal cache assignments and asymmetric channel conditions. More specifically, the first scheme combines the piggyback-coding idea in [2] with super-

position coding, and the second combines it with Maddah-Ali and Niesen's coded caching [1].

The new upper bound improves on the existing ones in [3, 14]. It coincides with the superposition piggyback-coding lower bound when only the weakest receiver is provided with a cache memory and the size is below a certain threshold (which depends on the BC statistics). It coincides with the generalized coded-caching lower bound for a particular cache assignment that assigns larger cache memories to the weaker receivers.

The proposed upper and lower bounds suggest that in a good cache assignment, weaker receivers are provided with larger cache memories compared to the stronger receivers. To make this statement more precise, we derive upper and lower bounds on the *global capacity-memory tradeoff*, where the cache assignment is optimized subject to a *total cache constraint*. The bounds coincide for small and large total cache memories. The lower bound is achieved by assigning all the cache memory to the weakest receiver when the total cache memory is small, and by distributing it among all the receivers when its size exceeds some threshold.

Numerical evaluations of the new upper bound confirm that uniform cache assignment is suboptimal in all regimes.

II. PROBLEM DEFINITION

Consider a transmitter and receivers $1, \dots, K$. The transmitter has access to a library with D independent messages, W_1, \dots, W_D , each distributed uniformly over the set $\{1, \dots, [2^{nR}]\}$. I.e. $R \geq 0$ denotes the rate of transmission and n is the transmission blocklength. In this work, we assume that there are more messages than receivers, $D \geq K$.

Each receiver $k \in \mathcal{K} := \{1, \dots, K\}$ is equipped with a cache of size $M_k \geq 0$. Communication takes place in two phases. For the first so called placement phase, the transmitter chooses caching functions $\{g_k : \{1, \dots, [2^{nR}]\}^D \rightarrow \{1, \dots, [2^{nM_k}]\}\}_{k=1}^K$ and places

$$\forall_k := g_k(W_1, \dots, W_D) \quad (1)$$

in receiver k 's cache.

The subsequent delivery phase takes place over a *degraded* BC [21] with finite input and output alphabets \mathcal{X} and $\mathcal{Y}_1, \dots, \mathcal{Y}_K$, and the channel transition law $\Gamma(y_1, \dots, y_K|x)$ that decomposes as follows for all $x \in \mathcal{X}$, $y_1 \in \mathcal{Y}_1, \dots, y_K \in \mathcal{Y}_K$:

$$\Gamma(y_1, \dots, y_K|x) = \Gamma_K(y_K|x) \cdot \Gamma_{K-1}(y_{K-1}|y_K) \cdots \Gamma_1(y_1|y_2).$$

Without loss of generality, we order the receivers $1, \dots, K$ from the weakest to the strongest.

At the beginning of the delivery phase, each receiver $k \in \mathcal{K}$ demands the message W_{d_k} , $d_k \in \{1, \dots, D\}$. The transmitter and all the receivers are informed of the demand vector $\mathbf{d} = (d_1, \dots, d_K)$. Using this information, the transmitter forms the channel input sequence $X^n := (X_1, \dots, X_n)$,

$$X^n = f_{\mathbf{d}}(W_1, \dots, W_D), \quad (2)$$

using the encoding function $f_{\mathbf{d}} : \{1, \dots, \lfloor 2^{nR} \rfloor\}^D \rightarrow \mathcal{X}^n$.

Receiver $k \in \mathcal{K}$ observes the channel output sequence $Y_k^n = (Y_{k,1}, \dots, Y_{k,n})$. Given the demand vector \mathbf{d} , the cache content \mathbb{V}_k , and the channel output Y_k^n , it produces its estimate of the desired message W_{d_k} as

$$\hat{W}_k := \varphi_{k,\mathbf{d}}(Y_k^n, \mathbb{V}_k), \quad (3)$$

where $\varphi_{k,\mathbf{d}} : \mathcal{Y}_k^n \times \{1, \dots, \lfloor 2^{nM_k} \rfloor\} \rightarrow \{1, \dots, \lfloor 2^{nR} \rfloor\}$ is the decoding function.

The worst-case probability of error at any receiver and for any demand \mathbf{d} is given by

$$P_e := \mathbb{P} \left[\bigcup_{\mathbf{d} \in \mathcal{D}} \bigcup_{k=1}^K \{\hat{W}_k \neq W_{d_k}\} \right].$$

A rate-memory tuple (R, M_1, \dots, M_K) is *achievable* if for any $\epsilon > 0$ there exists a large enough blocklength n and caching, encoding, and decoding functions (1)–(3) so that $P_e \leq \epsilon$.

Definition 1: The *capacity-memory tradeoff* $\mathcal{C}(M_1, \dots, M_K)$ is the largest rate R for which the rate-memory tuple (R, M_1, \dots, M_K) is achievable:

$$\mathcal{C}(M_1, \dots, M_K) := \sup\{R : (R, M_1, \dots, M_K) \text{ achievable}\}.$$

Our main goal in this paper is to optimize the cache assignment (M_1, \dots, M_K) to attain the largest capacity-memory tradeoff $\mathcal{C}(M_1, \dots, M_K)$ under the total cache constraint

$$\sum_{k=1}^K M_k \leq M. \quad (4)$$

Definition 2: The *global capacity-memory tradeoff* is

$$\mathcal{C}^*(M) := \max_{\substack{M_1, \dots, M_K > 0 \\ \sum_{k=1}^K M_k \leq M}} \mathcal{C}(M_1, \dots, M_K). \quad (5)$$

Remark 1: The global capacity memory tradeoff depends on the BC law $\Gamma(y_1, \dots, y_K|x)$ only through its marginal conditional laws. All our results thus also apply to *stochastically degraded BCs* [21].

Without cache memories, i.e., $M_1 = \dots = M_K = 0$, we have

$$\mathcal{C}(M_1 = 0, \dots, M_K = 0) = \mathcal{C}_{\mathcal{K}} \quad (6)$$

where $\mathcal{C}_{\mathcal{K}}$ is (see [21]):

$$\mathcal{C}_{\mathcal{K}} := \max \min \{I(U_1; Y_1), I(U_2; Y_2|U_1), \dots, I(U_{K-1}; Y_{K-1}|U_{K-2}), I(X; Y_K|U_{K-1})\}. \quad (7)$$

The maximization in (7) is over all auxiliary random variables $U_1, \dots, U_{K-1}, X, Y_1, \dots, Y_K$ that satisfy

$$P_{Y_1 \dots Y_K|X}(y_1, \dots, y_K|x) = \Gamma(y_1, \dots, y_K|x) \quad (8)$$

and form the Markov chain $U_1 - \dots - U_{K-1} - X - (Y_1, \dots, Y_K)$.

III. RESULTS ON THE CAPACITY-MEMORY TRADEOFF

A. Upper Bound on Capacity-Memory Tradeoff

The upper bound is formulated in terms of the following parameters. For each receiver set

$$\mathcal{S} = \{j_1, \dots, j_{|\mathcal{S}|}\} \subseteq \mathcal{K}, \quad j_1 < \dots < j_{|\mathcal{S}|}, \quad (9)$$

define

$$\alpha_{\mathcal{S},1}^* := \frac{M_{j_1}}{D} \quad (10a)$$

and for $k \in \{2, \dots, |\mathcal{S}|\}$:

$$\alpha_{\mathcal{S},k}^* := \min \left\{ \frac{\sum_{i=1}^k M_{j_i}}{D - k + 1}, \frac{1}{|\mathcal{S}| - k + 1} \left(\frac{|\mathcal{S}|}{D} \sum_{i=1}^{|\mathcal{S}|} M_{j_i} - \sum_{i=1}^{k-1} \alpha_{\mathcal{S},i}^* \right) \right\}. \quad (10b)$$

Theorem 1: There exist random variables X, Y_1, \dots, Y_K and for every receiver set \mathcal{S} as in (9) random variables $\{U_{\mathcal{S},1}, \dots, U_{\mathcal{S},|\mathcal{S}|-1}\}$, such that (8) and the Markov chain

$$U_{\mathcal{S},1} - U_{\mathcal{S},2} - U_{\mathcal{S},|\mathcal{S}|} - \dots - U_{\mathcal{S},|\mathcal{S}|-1} - X - (Y_{j_1}, \dots, Y_{j_{|\mathcal{S}|}}) \quad (11)$$

are satisfied and the following inequalities hold for all \mathcal{S} :

$$\mathcal{C}(M_1, \dots, M_K) \leq I(U_{\mathcal{S},k}; Y_{j_k}|U_{\mathcal{S},k-1}) + \alpha_{\mathcal{S},k}^*, \quad \forall k \in \{1, \dots, |\mathcal{S}|-1\}, \quad (12a)$$

$$\mathcal{C}(M_1, \dots, M_K) \leq I(X; Y_{j_{|\mathcal{S}|}}|U_{\mathcal{S},|\mathcal{S}|-1}) + \alpha_{\mathcal{S},|\mathcal{S}|}^*. \quad (12b)$$

Proof: Omitted. See [23, Appendix A]. ■

The converse in Theorem 1 is weakened if the constraints in (12) are relaxed for certain receiver sets \mathcal{S} , or if in these constraints the input/output random variables $X, Y_{j_1}, \dots, Y_{j_{|\mathcal{S}|}}$ are allowed to depend on the receiver set \mathcal{S} . For the latter relaxation, Theorem 1 results in the following corollary.

Corollary 2: The rate-memory tuple (R, M_1, \dots, M_K) is achievable only if for every receiver set $\mathcal{S} \subseteq \mathcal{K}$:

$$(R - \alpha_{\mathcal{S},1}^*, R - \alpha_{\mathcal{S},2}^*, \dots, R - \alpha_{\mathcal{S},|\mathcal{S}|}^*) \in \mathcal{C}_{\mathcal{S}}, \quad (13)$$

where $\mathcal{C}_{\mathcal{S}}$ denotes the capacity region of the degraded BC with the receivers in \mathcal{S} (disregarding the receivers in $\mathcal{K} \setminus \mathcal{S}$) when there are no cache memories [21].

Remark 2: The upper bounds in Theorem 1 and Corollary 2 are loosened if we replace the parameters $\{\alpha_{\mathcal{S},k}^*\}$ by

$$\tilde{\alpha}_{\mathcal{S},k} = \frac{\sum_{i=1}^k M_{j_i}}{D - k + 1}. \quad (14)$$

The same is also true if they are replaced by

$$\alpha'_{S,k} = \frac{1}{D} \sum_{i=1}^{|\mathcal{S}|} M_{j_i}. \quad (15)$$

In particular, replacing the parameters $\{\alpha_{S,k}^*\}$ by the parameters $\{\alpha'_{S,k}\}$ in Corollary 2, we recover the upper bounds in [3, Theorem 9] and [14, Theorem 1]. Note that while $\alpha'_{S,k}$ depends on the entire cache size that is available at the receivers in \mathcal{S} , $\alpha_{S,k}^*$ depends only on the size of the caches at receiver k and those receivers in \mathcal{S} that are weaker.

B. Lower Bounds on the Capacity-Memory Tradeoff

We start with a general lower bound that simply exploits the local caching gain. Similar to [20, Proposition 1], we have:

Proposition 3 (Local caching gain): For all $\Delta > 0$:

$$C(M_1 + \Delta, \dots, M_K + \Delta) \geq C(M_1, \dots, M_K) + \frac{\Delta}{D}. \quad (16)$$

Proof: The lower bound is achieved by storing a rate- $\frac{\Delta}{D}$ submessage of every message of the library in the cache memory of every receiver. These submessages can be retrieved locally and thus not be transmitted in the delivery phase. ■

We next present two lower bounds on the capacity-memory tradeoff based on the coding schemes sketched in Sections V and VI. The first assigns a cache memory only to the weakest receiver and the second assigns a cache memory to every receiver, but such that weaker receivers are assigned larger cache memories compared to the stronger receivers.

Let $(U_1^*, \dots, U_{K-1}^*, X^*)$ be a K -tuple of random variables that achieves the symmetric-capacity $C_{\mathcal{K}}$; i.e., it is a solution to the optimization problem in (7). Define

$$M_1^s := \frac{D}{K-1} (I(U_1^*; Y_2) - I(U_1^*; Y_1)). \quad (17)$$

Theorem 4 (Superposition Piggyback Coding): When $M_1 \leq M_1^s$, then irrespective of cache sizes M_2, \dots, M_K we have

$$C(M_1, \dots, M_K) \geq C_{\mathcal{K}} + \frac{M_1}{D}, \quad M_1 \leq M_1^s. \quad (18)$$

Proof: Achieved by the scheme in Section V, see [23, Section IV-B]. ■

For each $t \in \mathcal{K}$, let

$$\mathcal{G}_1^{(t)}, \dots, \mathcal{G}_{\binom{K}{t}}^{(t)} \quad (19)$$

denote all the subsets of $\{1, \dots, K\}$ that are of size t . Also, let $\mathcal{G}_\ell^{(t),c} := \{1, \dots, K\} \setminus \mathcal{G}_\ell^{(t)}$ for all $\ell \in \{1, \dots, \binom{K}{t}\}$. For a given input distribution P_X , define for $t \in \{1, \dots, K-1\}$ and $k \in \mathcal{K}$ the memory sizes

$$M_k^{(t)} := D \cdot \frac{\sum_{\{\ell: k \in \mathcal{G}_\ell^{(t)}\}} \prod_{k' \in \mathcal{G}_\ell^{(t),c}} I(X; Y_{k'})}{\sum_{j=1}^{\binom{K}{t+1}} \prod_{k' \in \mathcal{G}_j^{(t+1),c}} I(X; Y_{k'})}, \quad (20a)$$

where the denominator is defined to be 1 when $t = K-1$. Also, for each $t \in \{1, \dots, K-1\}$, define the transmission rate

$$R^{(t)} := \frac{\sum_{\ell=1}^{\binom{K}{t}} \prod_{k' \in \mathcal{G}_\ell^{(t),c}} I(X; Y_{k'})}{\sum_{j=1}^{\binom{K}{t+1}} \prod_{k' \in \mathcal{G}_j^{(t+1),c}} I(X; Y_{k'})}, \quad (20b)$$

where the denominator is again 1 for $t = K-1$.

Theorem 5 (Generalized Coded Caching): Fix an input distribution P_X . For each $t \in \{1, \dots, K-1\}$:

$$C(M_1^{(t)}, \dots, M_K^{(t)}) \geq R^{(t)}, \quad (21)$$

where $M_1^{(t)}, \dots, M_K^{(t)}$ and $R^{(t)}$ are defined by (20) for the chosen input distribution P_X .

Proof: The lower bound is achieved by the scheme outlined in Section VI, see [23, Section IV-C]. ■

C. Exact Results

The upper and lower bounds match in two special cases.

Proposition 6 (Small Cache Memory at the Weakest Receiver): Suppose $M_1 > 0$ and $M_2 = \dots = M_K = 0$. Then,

$$C(M_1, 0, \dots, 0) = C_{\mathcal{K}} + \frac{M_1}{D}, \quad M_1 \leq M_1^s, \quad (22)$$

where M_1^s is defined in (17).

Proof: The achievability is by superposition piggyback coding, see Theorem 4, and the converse is by Corollary 2, where one only considers $\mathcal{S} = \mathcal{K}$. ■

The performance in (22) corresponds to a *perfect* global caching gain where each and every receiver can benefit from receiver 1's *cache content* as if it was locally present.

Proposition 7 (Large Cache Memories): For each $k \in \mathcal{K}$, let $M_k^{*(K-1)}$ be given by (20a) when P_X is a maximizer of

$$C_{\text{Avg}} := \frac{1}{K} \cdot \max_{P_X} \sum_{k=1}^K I(X; Y_k). \quad (23)$$

For any $\Delta \geq 0$:

$$\begin{aligned} & C(M_1^{*(K-1)} + \Delta, \dots, M_K^{*(K-1)} + \Delta) \\ &= C_{\text{Avg}} + \frac{\sum_{k=1}^K M_k^{*(K-1)}}{K \cdot D} + \frac{\Delta}{D}. \end{aligned} \quad (24)$$

Proof: For $\Delta = 0$, the achievability is by the generalized coded caching with $t = K-1$, see Theorem 5. For $\Delta > 0$, it follows from the case $\Delta = 0$ and from Proposition 3. The converse follows from Theorem 1 for $\mathcal{S} = \{k\}$, $k \in \mathcal{K}$, by relaxing the parameters $\alpha_{S,k}^*$ to $\alpha'_{S,k}$. ■

IV. RESULTS ON THE GLOBAL CAPACITY-MEMORY TRADEOFF

A. Upper Bounds

Theorem 1 directly yields the following result.

Theorem 8 (Upper Bound): There exist random variables X, Y_1, \dots, Y_K and for every receiver set \mathcal{S} as in (9) random variables $\{U_{S,1}, \dots, U_{S,|\mathcal{S}|-1}\}$, so that (8) and (11) hold, and so that for some $M_1, \dots, M_K \geq 0$ summing to M and all \mathcal{S} :

$$\begin{aligned} C^*(M) &\leq I(U_{S,k}; Y_k | U_{S,k-1}) + \alpha_{S,k}^*, \quad k \leq |\mathcal{S}| - 1 \\ C^*(M) &\leq I(X; Y_{j_{|\mathcal{S}|}} | U_{S,|\mathcal{S}|-1}) + \alpha_{S,|\mathcal{S}|}^* \end{aligned} \quad (25)$$

where $\{\alpha_{S,k}^*\}$ are defined in (10).

Solving this optimization problem numerically is computationally complex. Simpler, albeit looser, upper bounds can

be obtained by either ignoring some of the constraints in (25); replacing the parameters $\alpha_{\mathcal{S},k}^*$ by $\tilde{\alpha}_{\mathcal{S},k}$ or $\alpha'_{\mathcal{S},k}$ (see (14), (15)); or allowing $X, Y_{j_1}, \dots, Y_{j_S}$ in (25) to depend on the set \mathcal{S} . We present three simpler bounds that will be used in the sequel.

Corollary 9:

$$C^*(M) \leq C_{\mathcal{K}} + \frac{M}{D} \quad (26)$$

and

$$C^*(M) \leq C_{\text{avg}} + \frac{1}{K} \cdot \frac{M}{D}. \quad (27)$$

For each set $\mathcal{G}_\ell^{(t)}$ in (19), let $C_{\mathcal{G}_\ell^{(t)}}$ be the largest symmetric rate that can be achieved at the receivers in $\mathcal{G}_\ell^{(t)}$ (ignoring the receivers in $\mathcal{K} \setminus \mathcal{G}_\ell^{(t)}$) when there are no cache memories.

Corollary 10: For each $t \in \mathcal{K}$:

$$C^*(M) \leq \frac{1}{\binom{K}{t}} \sum_{\ell=1}^{\binom{K}{t}} C_{\mathcal{G}_\ell^{(t)}} + \frac{t}{K} \cdot \frac{M}{D}. \quad (28)$$

B. Lower Bound

Let $M^{(0)} := 0$ and $M^s := M_1^s$ (see (17)), and let $R^{(0)} := C_{\mathcal{K}}$ and $R^s := C_{\mathcal{K}} + \frac{M^s}{D}$. Also, recall $M_k^{(t)}$ and $R^{(t)}$ from (20) and define $M^{(t)} := \sum_{k=1}^K M_k^{(t)}$ for $t \in \{1, \dots, K-1\}$. Note that

$$R^{(t)} := \frac{M^{(t)}}{t}. \quad (29)$$

Theorem 11 (Lower Bound):

$$C^*(M) \geq \text{upp hull} \left\{ (R^{(0)}, M^{(0)}), (R^s, M^s), (R^{(1)}, M^{(1)}), \dots, (R^{(K-1)}, M^{(K-1)}) \right\}. \quad (30)$$

Proof: The proof follows by Theorems 4 and 5, and uses standard memory-sharing arguments [1, 3]. ■

C. Exact Results

Proposition 12 (Small M): For small total cache size M :

$$C^*(M) = C_{\mathcal{K}} + \frac{M}{D}, \quad M \leq M^s. \quad (31)$$

Proof: The achievability follows from Theorem 11 and the converse is by inequality (26). ■

Define

$$M^L = D \cdot (K-1) \cdot K \cdot C_{\text{avg}} \quad (32)$$

where C_{avg} is defined in (23).

Proposition 13 (Large M): For large total cache sizes:

$$C^*(M) = K C_{\text{avg}} + \frac{1}{K} \cdot \frac{M - M^L}{D}, \quad M \geq M^L. \quad (33)$$

Proof: The achievability is by Propositions 3 and 7, and the converse is by inequality (27), see [23, Corollary 11]. ■

For small total cache sizes, $C^*(M)$ grows as $\frac{M}{D}$. This corresponds to a perfect global caching gain as if each receiver could access all cache contents in the network locally. For large total cache sizes, the global benefit of receivers' cache memories is fully exploited. Any additional cache budget

exceeding M^L should be distributed among the receivers uniformly and it only offers a local caching gain. In particular, $C^*(M)$ grows as $\frac{1}{K} \cdot \frac{M}{D}$. In the moderate regime of $\frac{M}{D}$, the slope with which $C^*(M)$ scales with $\frac{M}{D}$ is given by

$$\frac{R^{(t+1)} - R^{(t)}}{\frac{M^{(t+1)}}{D} - \frac{M^{(t)}}{D}} = \frac{1}{t+1} - \frac{M^{(t)}}{t(t+1)(M^{(t+1)} - M^{(t)})},$$

for $t = 1, \dots, K-1$, and it is close to $\frac{1}{t+1}$ for large t .

Example 1 (4-User Gaussian BC): Consider a $K = 4$ receiver Gaussian BC. At time t , receiver k observes

$$Y_{k,t} = X_t + Z_{k,t}, \quad (34)$$

where X_t is the transmitter's channel input and $\{Z_{k,t}\}$ a sequence of independent and identically distributed centered Gaussian noise with variance $\sigma_k^2 > 0$. We impose an average block-power constraint $P > 0$ in the channel input and order the receivers in increasing strength:

$$\sigma_1^2 = 4 \geq \sigma_2^2 = 1 \geq \sigma_3^2 = 0.5 \geq \sigma_4^2 = 0.1 > 0.$$

For Gaussian BCs, a zero-mean variance- P Gaussian input distribution P_X maximizes $I(X; Y_k)$ simultaneously for all $k \in \mathcal{K}$ under an input power constraint P . Therefore,

$$C_{\text{avg}} = \frac{1}{K} \sum_{k=1}^K \frac{1}{2} \log \left(1 + \frac{P}{\sigma_k^2} \right) = \frac{1}{K} \sum_{k=1}^K C_k. \quad (35)$$

Figure 1 shows the upper and lower bounds on $C^*(M)$ in Theorems 8 and 11. The five blue points indicate the rate-memory points $(R^{(0)}, M^{(0)})$, (R^s, M^s) , $(R^{(1)}, M^{(1)})$, $(R^{(2)}, M^{(2)})$, and $(R^{(3)}, M^{(3)})$. For comparison, the figure also shows the upper bound in Theorem 1 for a setup with uniform cache assignment $\frac{M}{K}$ across all receivers. We observe that a smart cache assignment provides substantial gains in global capacity-memory tradeoff.

In a recent work in [22], we considered Gaussian BCs and derived related, but looser, bounds on $C^*(M)$. In particular, the bounds in [22] do not coincide for small M .

V. SUPERPOSITION PIGGYBACK-CODING

We outline the scheme and refer to [23, Section IV-B] for details. Fix a tuple $(U_1^*, \dots, U_K^*, X^*)$ that maximizes (7), and construct a K -level superposition code according to the joint distribution of this tuple. Split $W_d = (W_d^{(A)}, W_d^{(B)})$, and cache messages $W_1^{(B)}, \dots, W_D^{(B)}$ at receiver 1.

The transmitter uses the superposition code to send:

$$\begin{aligned} &W_{d_1}^{(A)}, W_{d_2}^{(B)}, \dots, W_{d_{K-1}}^{(B)}, W_{d_K}^{(B)} && \text{in level 1} \\ &W_{d_k}^{(A)} && \text{in level } k \in \{2, \dots, K\}. \end{aligned}$$

Each receiver $k \in \{2, \dots, K\}$ decodes levels $1, 2, \dots, k$ using a standard decoder. Receiver 1 only decodes level 1 to which it applies a joint cache-channel decoding rule. It first retrieves messages $W_{d_2}^{(B)}, \dots, W_{d_K}^{(B)}$ from its cache memory, and extracts a subcodebook \mathcal{C}'_1 that contains all "compatible" level-1 codewords, i.e., all codewords encoding the tuple $(w, W_{d_2}^{(B)}, \dots, W_{d_K}^{(B)})$ for some $w \in \{1, \dots, [2^{nR^{(A)}}]\}$. It then

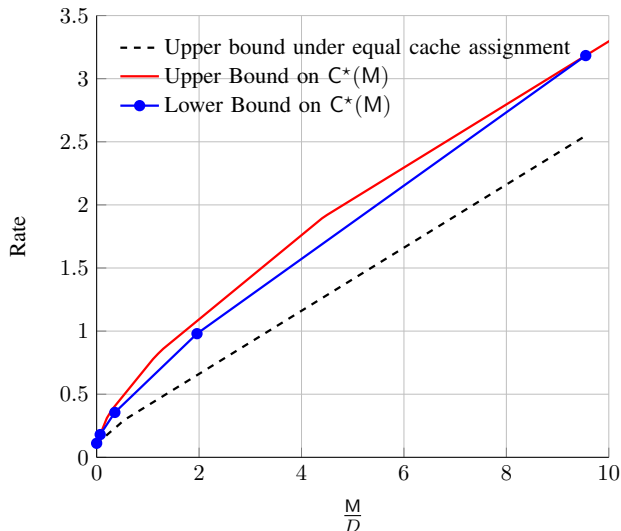


Fig. 1. Bounds on $C^*(M)$ for a 4-user Gaussian BC with $P = 1$, $\sigma_1^2 = 4$, $\sigma_2^2 = 1$, $\sigma_3^2 = 0.5$, $\sigma_4^2 = 0.1$. We have $D = 10$.

decodes its desired message $W_{d_1}^{(A)}$ using an optimal decoding rule for \mathcal{C}'_1 . Notice that the rate of \mathcal{C}'_1 is the rate of $W_{d_1}^{(A)}$, and receiver 1's decoding performance is not degraded by the fact that additional messages $W_{d_2}^{(B)}, \dots, W_{d_K}^{(B)}$ are sent in level 1.

VI. GENERALIZED CODED CACHING

We only sketch the scheme for $K = 2$ and $t = 1$. See [23, Section IV-C] for the general scheme and its analysis.

Split $W_d = (W_d^{(A)}, W_d^{(B)})$, where $R^{(A)} = I(X; Y_1) - \epsilon$ and $R^{(B)} = I(X; Y_2) - \epsilon$ for some input distribution P_X and a small $\epsilon > 0$. Cache messages $W_1^{(B)}, \dots, W_D^{(B)}$ at receiver 1 and messages $W_1^{(A)}, \dots, W_D^{(A)}$ at receiver 2.

The transmitter zero pads the binary representation of $W_{d_1}^{(A)}$ to $nR^{(B)}$ bits and sends its XOR with $W_{d_2}^{(B)}$ over the channel using a point-to-point code \mathcal{C} . Receiver 2 decodes the XOR-message directly from \mathcal{C} , and XORs it with the zero-padded version of $W_{d_1}^{(A)}$ stored in its cache memory. Receiver 1 in contrast performs joint cache-channel decoding: It retrieves $W_{d_2}^{(B)}$ from its cache memory, and extracts a subcodebook $\mathcal{C}' \subseteq \mathcal{C}$ containing all codewords that are "compatible" with $W_{d_2}^{(B)}$. It then decodes its desired message $W_{d_1}^{(A)}$ using an optimal decoding rule for subcodebook \mathcal{C}' . Notice that the rate of \mathcal{C}' is the rate of $W_{d_1}^{(A)}$, i.e., $I(X; Y_1) - \epsilon$.

VII. SUMMARY AND CONCLUSION

We provided close upper and lower bounds on the global capacity-memory tradeoff $C^*(M)$ of degraded BCs. The bounds coincide in the regimes of small and large total cache memory, characterized in terms of the BC statistics. Given a small total cache memory, it is optimal to assign it to the weakest receiver. In this regime, $C^*(M)$ grows as $\frac{M}{D}$ which corresponds to a perfect global caching gain where all receivers can benefit from all the cache contents of the network. In the regime of moderate M , the slope of $C^*(M)$

seems to decrease as M increases. In this regime, we propose to allocate cache memories to all the receivers, but such that weaker receivers are provided with larger cache memories (as specified in Theorem 5). Once the total cache memory budget exceeds a certain threshold, it is optimal to uniformly allocate all the remaining cache memory across all the receivers and to store the same content in the extra portions of the receivers' cache memories. Here, $C^*(M)$ grows as $\frac{1}{K} \cdot \frac{M}{D}$, which corresponds to a local caching gain.

We conclude that assigning the total cache memory uniformly across all the receivers is highly suboptimal over noisy broadcast channels in contrast to the noiseless setup in [1].

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